<u>**Specification Amendment**</u>

In the Title:

Please replace the current title with the following new title:

Method and apparatus for data speculation in an out-of-order processor utilizing test instructions and register renaming

In the Specification:

Please replace paragraph number [0049] with the following amended paragraph:

[0049] The FIG. 7B system may also include several processors, of which only two, processors 70, 80 are shown for clarity. Processors 70, 80 may each include a local memory channel hub (MCH) 72, 82 to connect with memory 2, 4. <u>Processors may also include processor cores 74 and 84.</u> Processors 70, 80 may exchange data via a point-to-point interface 50 using point-to-point interface circuits 78, 88. Processors 70, 80 may each exchange data with a chipset 90 via individual point-to-point interfaces 52, 54 using point to point interface circuits 76, 94, 86, 98. Chipset 90 may also exchange data with a high-performance graphics circuit 38 via a high-performance graphics interface 92.